

Technology News

Evatronix IP now available on CMP multi-project wafers

February 02, 2010 || 222600755

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LONDON — Evatronix SA and Circuits Multi-Projets (CMP) are collaborating on an educational program by including the distribution of the portfolio of Evatronix intellectual property (IP) for universities and research laboratories.

CMP (Grenoble, France) is a service of multi projects on wafers supporting several technologies. Since 1981, more than 1000 institutions from 66 countries have been served through 5200 projects on 600 Runs.

CMP is licensing Evatronix IPs to ease the access and shorten the time of designs' development.

The entire Evatronix (Bielsko-Biala, Poland) IP portfolio will be available through CMP - 8051 microcontrollers, USB 2.0 and 3.0 as well as SD and NAND flash memory controllers, multimedia IP and a variety of complementary solutions.

According to CMP, this move opens up new opportunities for the university educational program and adds new features to its actual IC's manufacturing service. Evatronix IPs will be available in several technology processes supported by CMP.

"Our expectation of this program is to help universities and laboratories bring their innovative ideas to life," said Wojciech Sakowski, Evatronix President and CEO. "With this very special pricing universities and research labs that will take part in the program will be able to focus on the added value of their designs rather than struggle with time and budget limitations."

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