



CNRS Grenoble INP UJF

Circuits Multi-Projets®
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Order form for
CMP Annual Report
2010

CMP is a service organization in ICs and MEMS for a number of technologies (prototyping and low volume production).

Integrated Circuits

Table with 3 columns: Technology/Process, Description, and Supplier. Rows include various CMOS and SOI processes from suppliers like Austriamicrosystems, STMicroelectronics, and Triquint Semiconductor.

Micro Electro Mechanical Systems (MEMS)

Table with 3 columns: Technology, Description, and Supplier. Rows include PolyMUMPs, SOIMUMPs, MetalMUMPs, and Bulk Micromachining.

3D Integrated Circuits (3D-IC)

Table with 3 columns: Technology, Description, and Supplier. Row includes 130 nm CMOS FaStack, 130 nm 2 Tiers 3D-IC, and Tezzaron/GlobalFoundries.

CMP also provides packaging (ceramic and plastic), CAD Tools (ARM suite of tools, Tanner L-Edit, Leonardo/ModelSim, tools from SoftMEMS, Evatronix, etc.), design kits for all the available processes for a number of CAD suites, and assistance for IP Exploitation (ARM cores on STMicroelectronics processes, RAMs, DP-RAMs,...)

CMP already served more than 1000 academic centres and industrial companies from 70 countries. More than 6000 different circuits have been fabricated for prototyping or low volume and 60 different technologies have been interfaced.

The report details the different services proposed for manufacturing, CAD procurement and assistance for IPs exploitation. The 2010 Annual Report is available upon request.

Please send me the 2010 CMP Annual Report: (free of charge).

Name:

Organization:

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..... Country:.....

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Please return by fax or by email to CMP: +33 476 47 38 14 or cmp@imag.fr

A version of the 2010 CMP Annual Report is also available on the web: http://cmp.imag.fr