



CNRS – INPG – UJF

# Design kits



Chapitre  
Français



Section  
France

- Procedure to get a design kit
- Design kits available at CMP
- Design kits distribution



# Procedure to handle DKs requests



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<http://cmp.imag.fr>

Customer Request

NDA Edition

NDA

Signed NDA reception

Foundries Agreement

No

Yes

Re-targeting

No

Yes

Export Agreement

No

Yes

Give up

Design kit & Design rules shipment  
(CD, DVD, WEB)



# Design kit request form



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<http://cmp.imag.fr/>

Circuits Multi-Projets®  
Multi-Project Circuits®

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Products :: Design-Kits :: Design-Kits Request

DESIGN KIT REQUEST FORM		
Foundry *	<input type="text"/>	
Technology Processes *	<input type="text"/>	
CAD Software and Version *	<input type="text"/>	
Institution *	<input type="text"/>	
Department	<input type="text"/>	
Name of the responsible person who will sign the Confidentiality Agreement: (person duly authorized to execute the agreement)	First Name	<input type="text"/>
	Last Name *	<input type="text"/>
	Position *	<input type="text"/>
	Telephone *	<input type="text"/>
	Fax *	<input type="text"/>
TECHNICAL CONTACT NAME	First Name	<input type="text"/>
	Last Name *	<input type="text"/>
	Position	<input type="text"/>
	Telephone *	<input type="text"/>
	Fax *	<input type="text"/>
ADDRESS for the shipment (no P.O. Box):	Address 1 *	<input type="text"/>
	Address 2	<input type="text"/>
	ZIP Code *	<input type="text"/>
	State	<input type="text"/>
	City *	<input type="text"/>
Country *	<input type="text"/>	
	<input type="text"/>	
Technical specifications of your project *	<input type="text"/>	
Application area of your project *	<input type="text"/>	
Verification *	For added security, please enter the verification code hidden in the image :	
	 <input type="text"/>	
An e-mail is automatically generated and sent to CMP.		
<input type="button" value="Send Design-Kit Request"/>		



# Design Kit & DRM Distribution



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	<b>Design Kit</b>	<b>DRM</b>
Austriamicrosystems	FTP	Online
STMicroelectronics	FTP	On Controlled paper
TEZZARON	FTP	PDF
MEMSCAP	FTP	Online



# Supported Design Kits



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<http://cmp.imag.fr/products/DK>

<b>Austriamicrosys.</b>	<b>CMOS 0.35<math>\mu</math>m C35B4C3</b>	<b>Cadence Mentor Graphics Synopsys Tanner Tools</b>
	<b>SiGe BiCMOS 0.35<math>\mu</math>m S35D4</b>	<b>Cadence Mentor Graphics Synopsys</b>
	<b>HV CMOS 0.35<math>\mu</math>m H35B4D3</b>	<b>Cadence Mentor Graphics Synopsys</b>



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# Supported Design Kits



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<http://cmp.imag.fr/products/DK>

<b>STMicroelectronics</b>	<b>CMOS040 40nm CMOS CMOS065 65nm CMOS CMOS065-SOI 65nm SOI HCMOS9 130nm CMOS HCMOS9-SOI 130nm SOI BiCMOS9-MW</b>	<b>Cadence Synopsys</b>
<b>Tezzaron/ Global Foundries</b>	<b>3D-IC 130nm CMOS</b>	<b>Cadence Synopsys</b>



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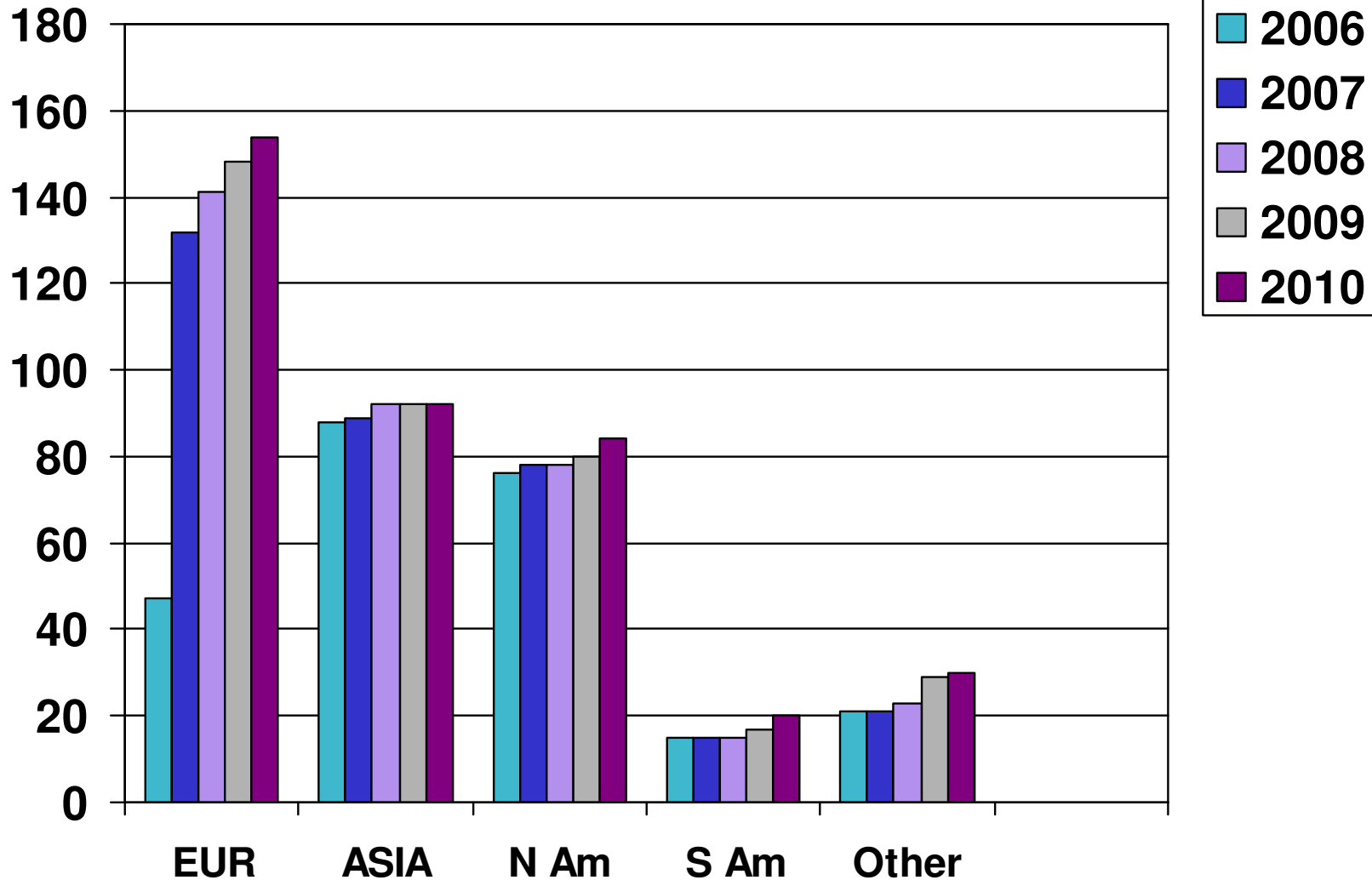
# Austriamicrosystems institutions



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- **14 New Institutions in 2010**
- **Total cumulated of 380 Institutions**



# STMicronics Institutions

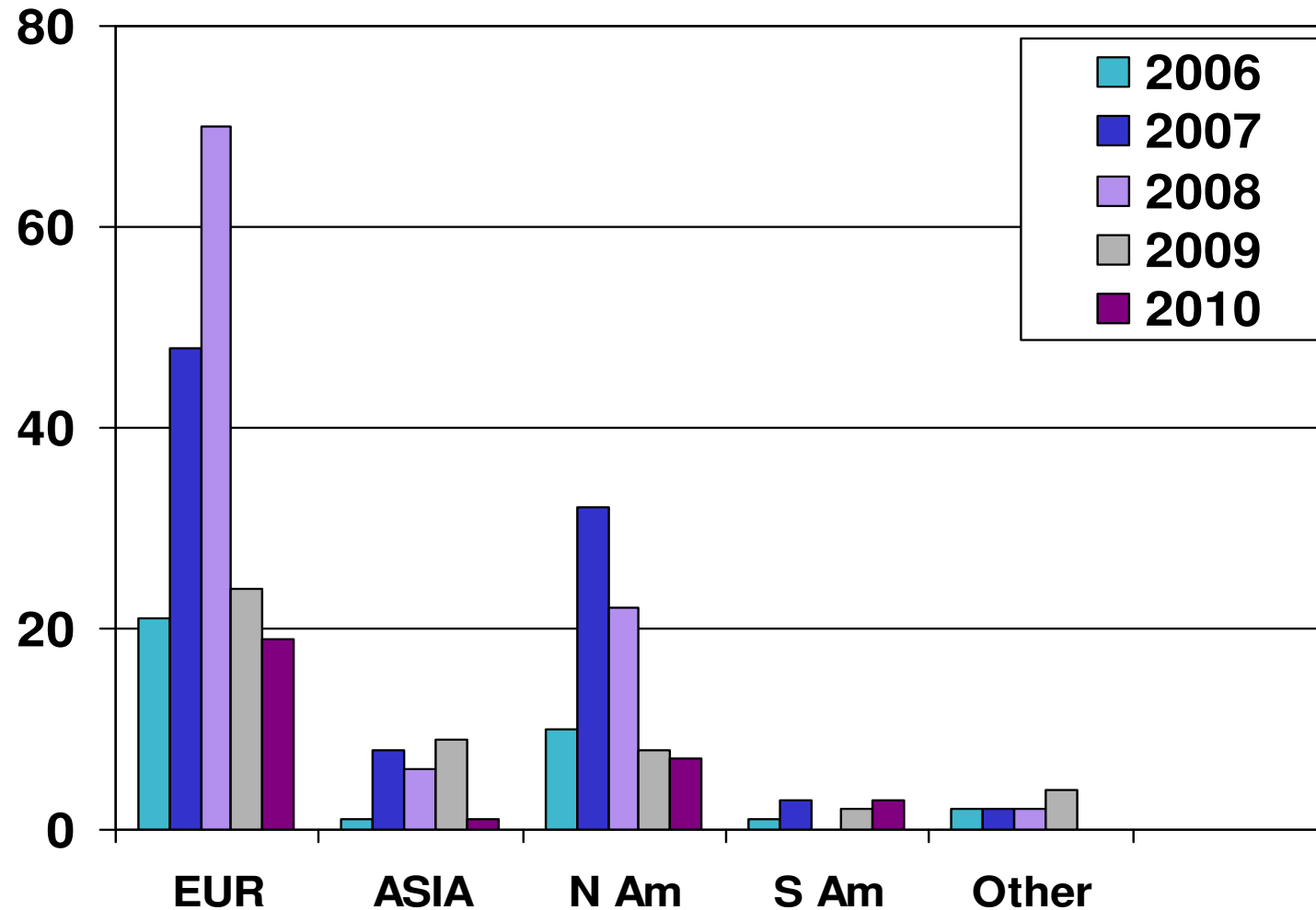


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• 30 New Institutions in 2010



# STMicroelectronics DK Distrib.



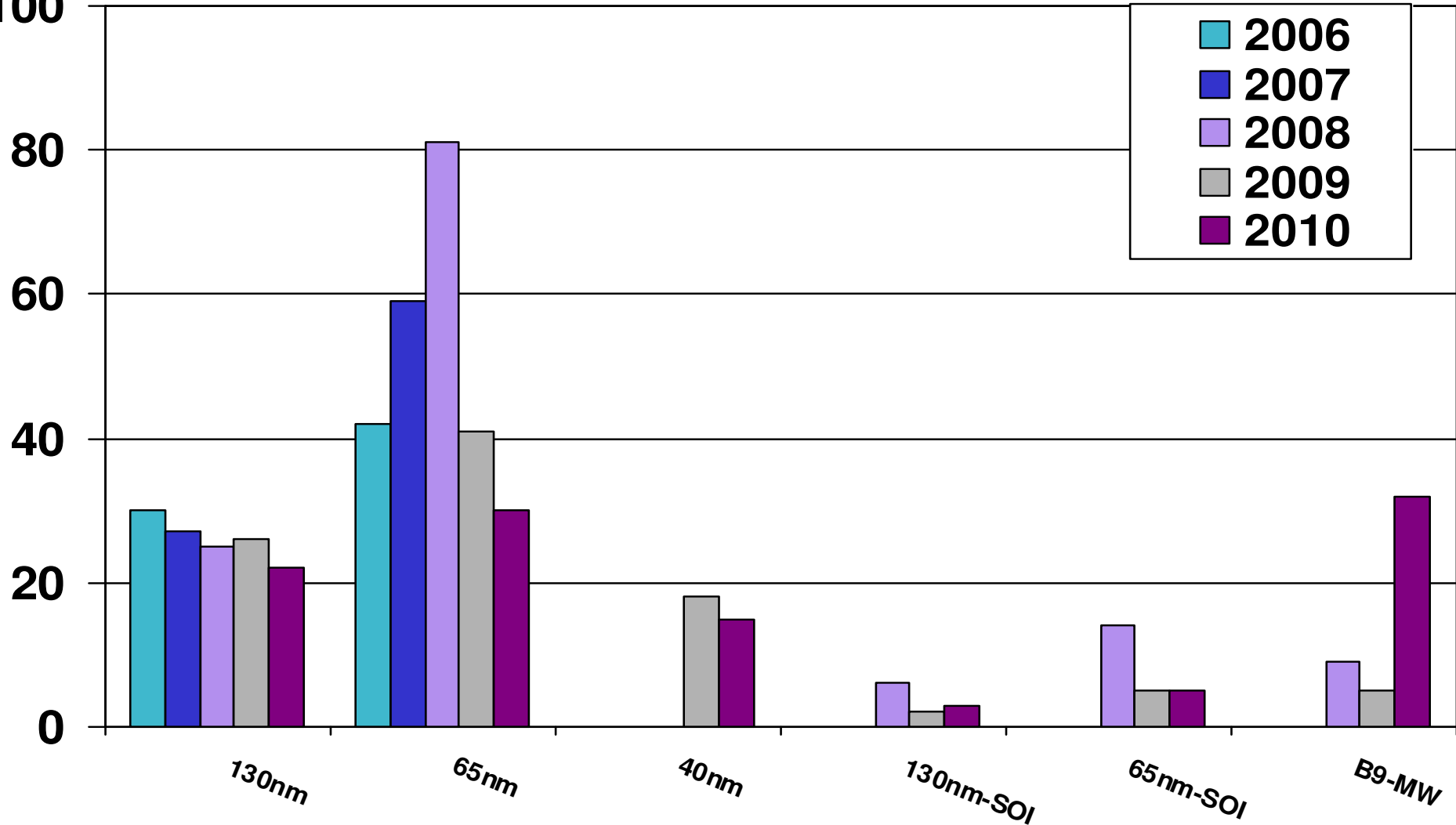
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100



**108 DK distributions done in 2010**



# STMicroelectronics DK Distrib.

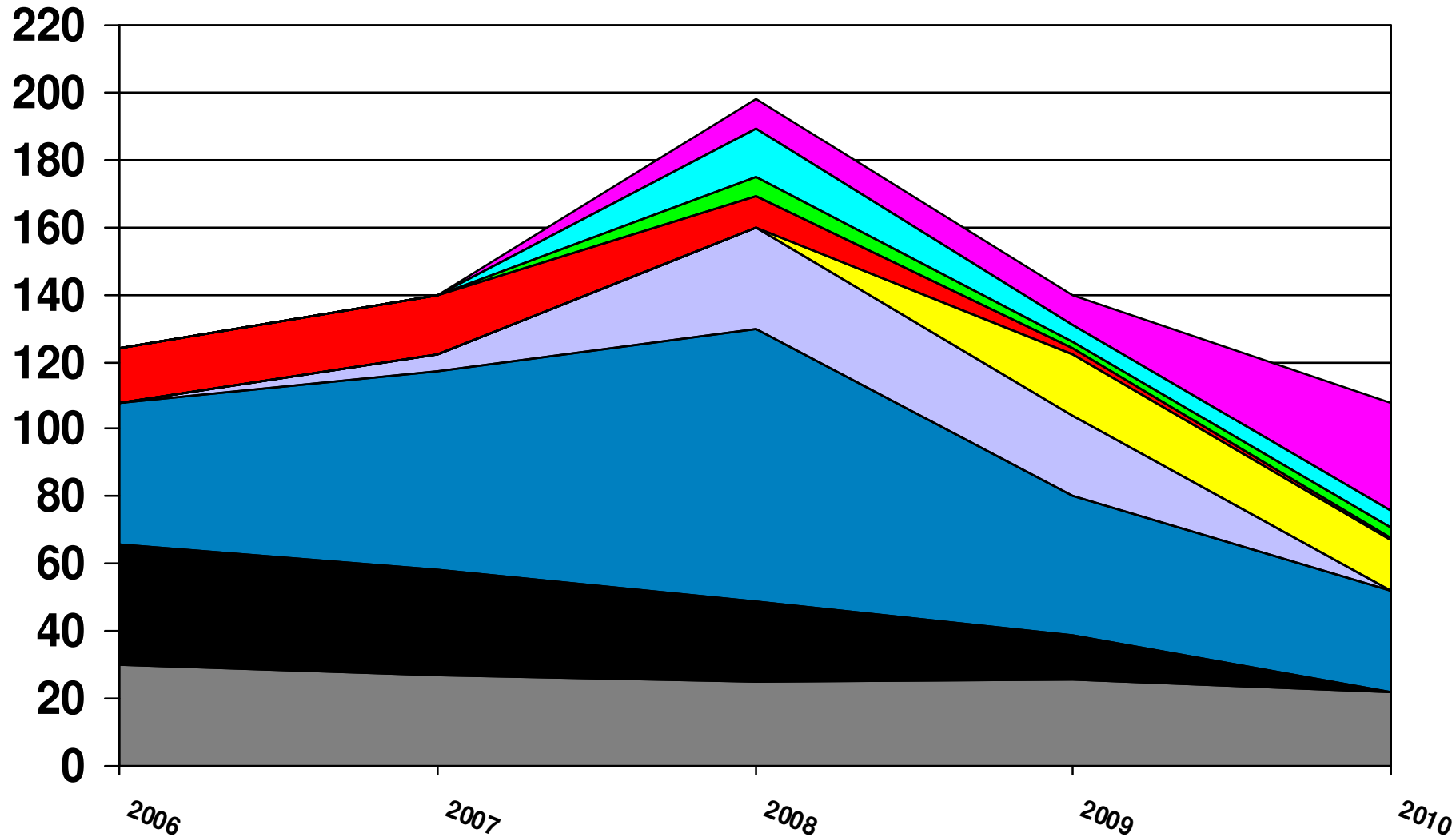


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■ 130nm ■ 90nm ■ 65nm ■ 45nm ■ 40nm ■ 0,25 SiGe ■ 130nm-SOI ■ 65nm-SOI ■ B9-MW



# Distribution in Europe



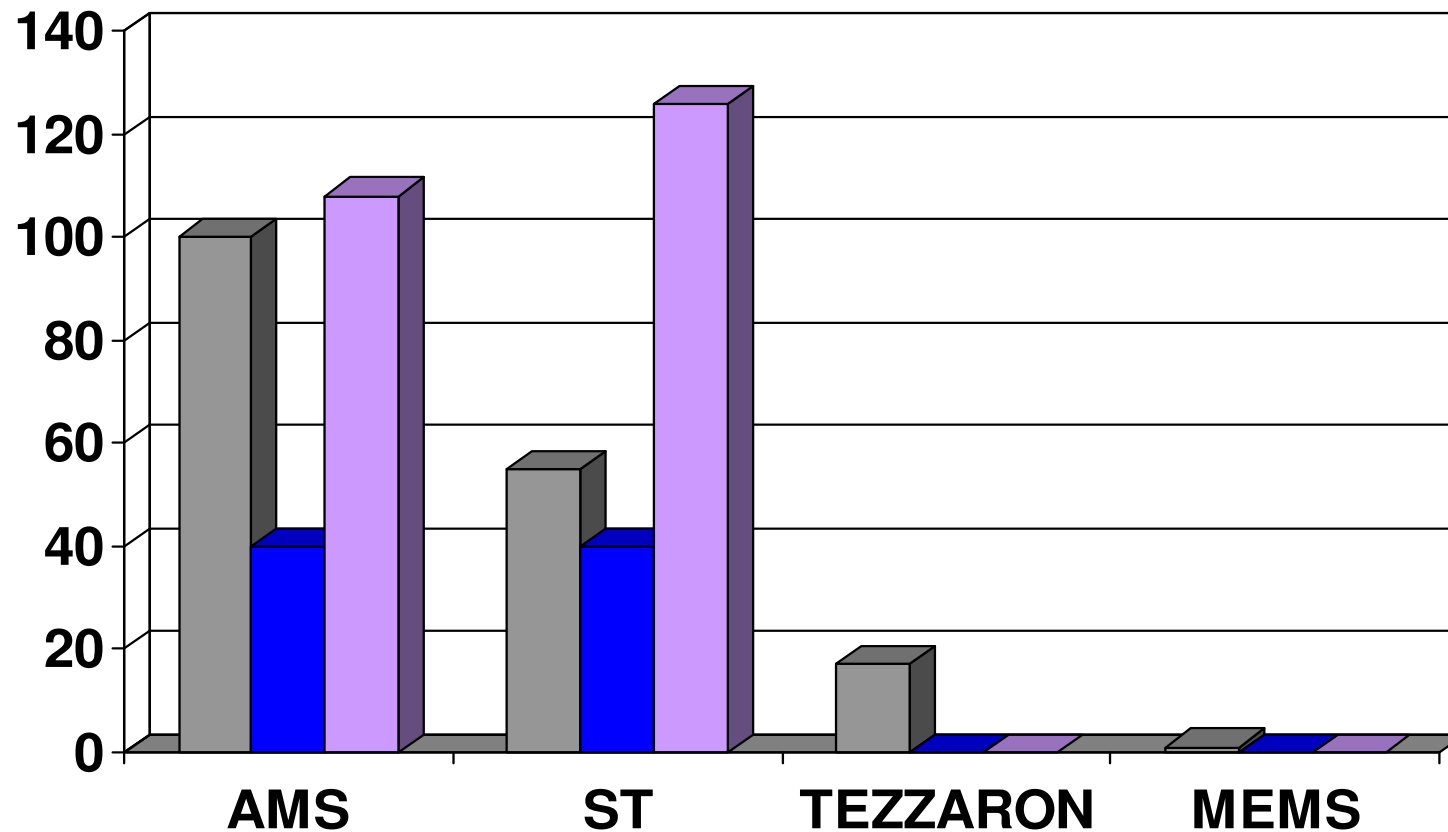
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- Institutions which have received Design Kits in 2010
- Institutions which have sent circuits in 2010
- Total circuits in 2010





# Rest of the world



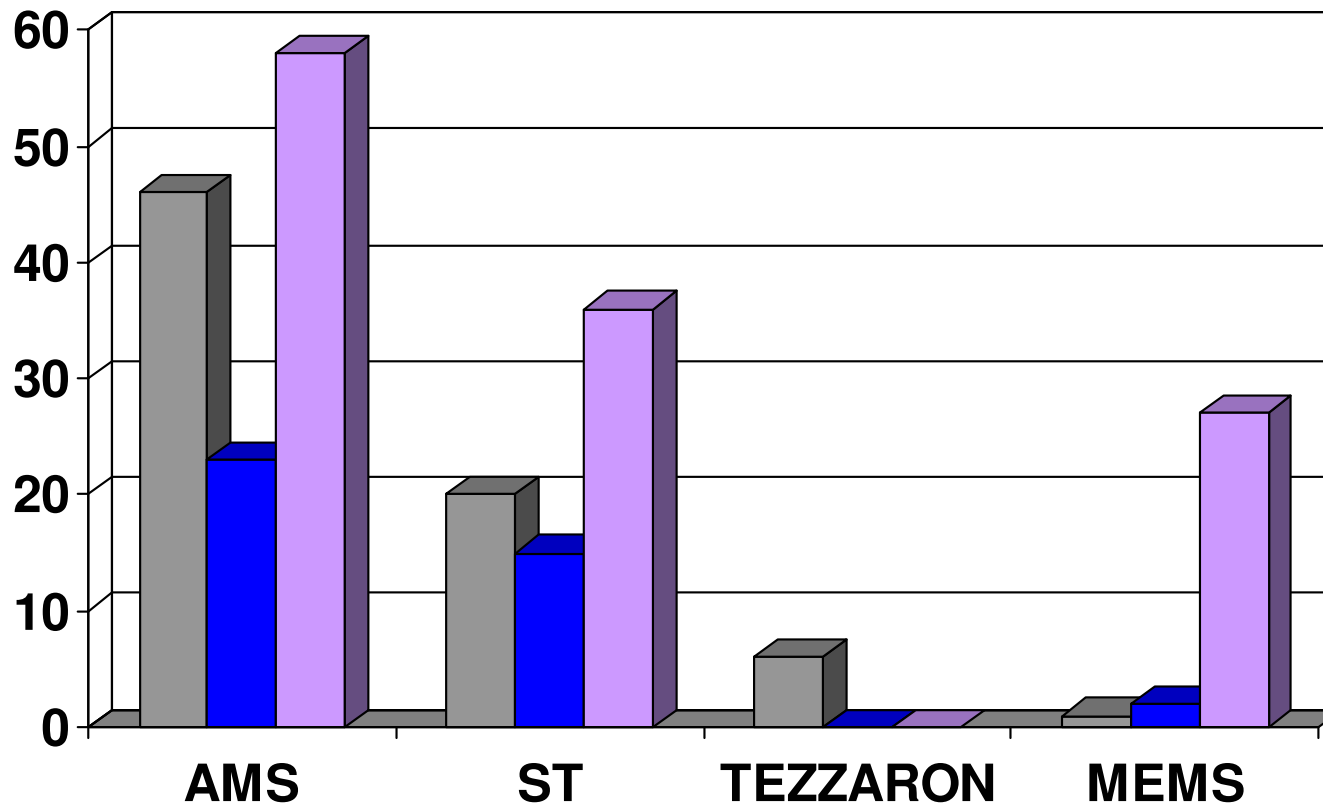
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- Institutions which have received Design Kits in 2010
- Institutions which have sent circuits in 2010
- Total circuits in 2010





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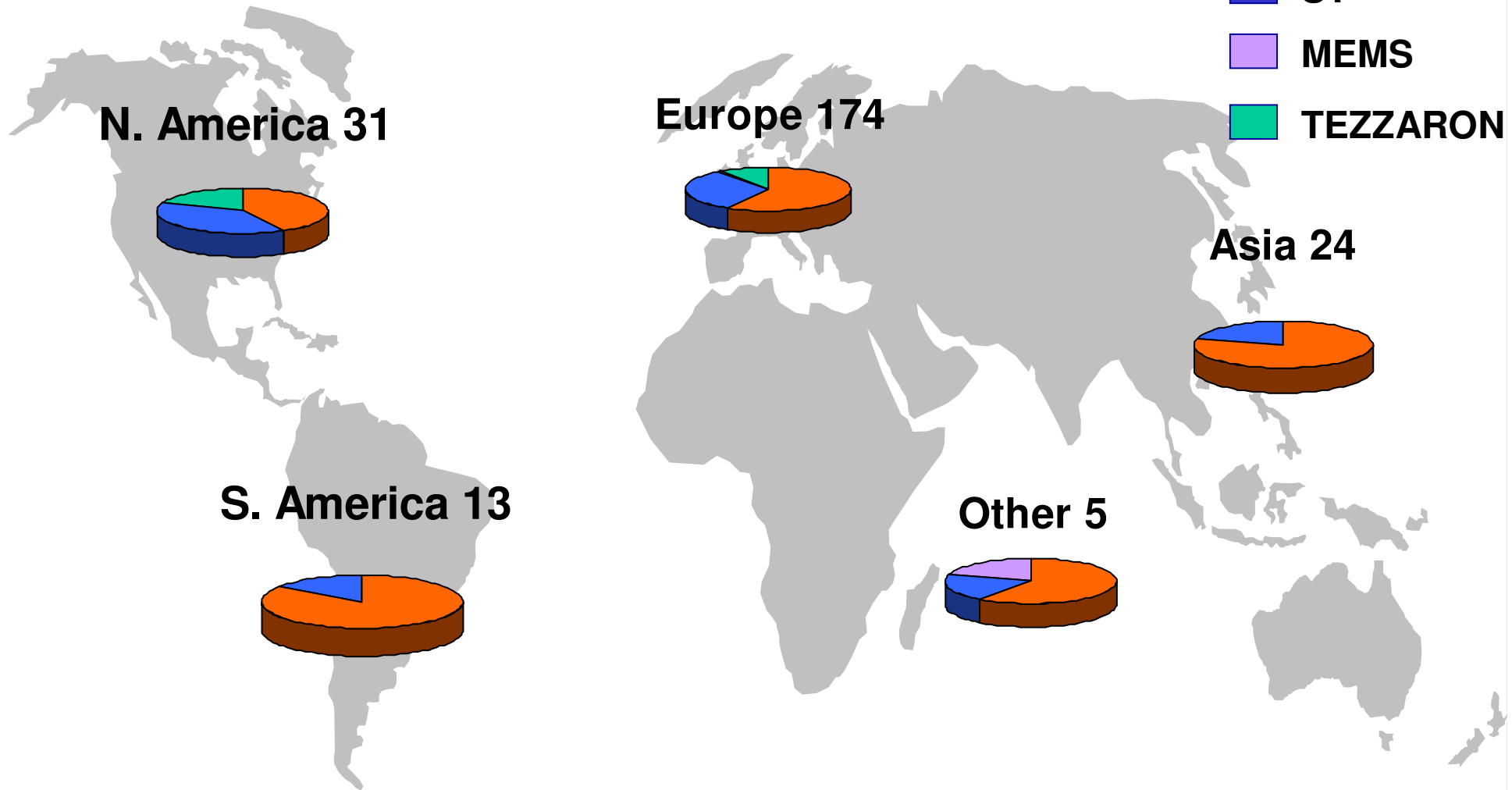
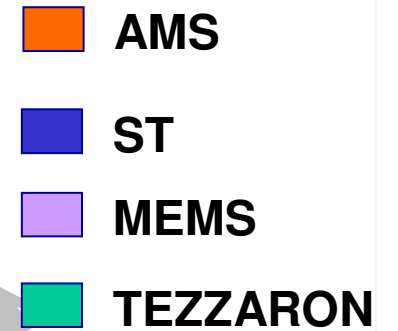
# Design Kits distribution in 2010



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# Design Kit distribution



- **Distribution and support**
  - ✓ FTP, CD-ROM, DVD
  - ✓ Licence files
  - ✓ Release distribution
  - ✓ Delay constraints (Foundries agreement, Export authorization)
- **Web information**
  - ✓ Design kit list & regular updates
  - ✓ Procedure to get design kits