

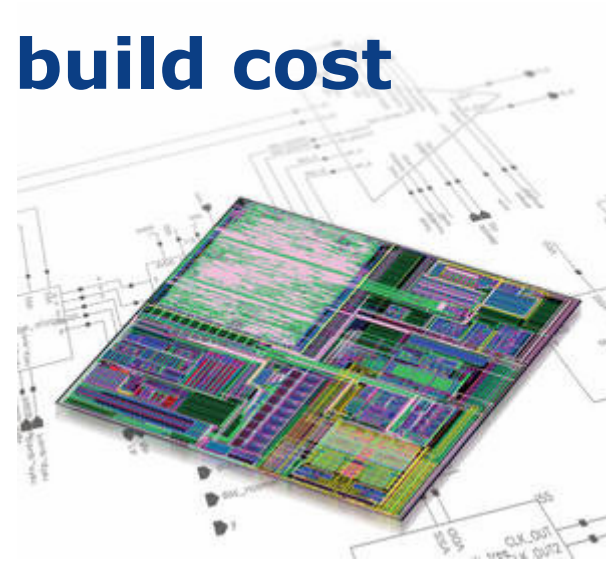
Evatronix & CMP

An Introduction

Michal Jedrak
Technical Marketing Manager

OUR MISSION

- ◆ **Develop “must-have” digital and analog IP for our customers to build cost effective SoC solutions**

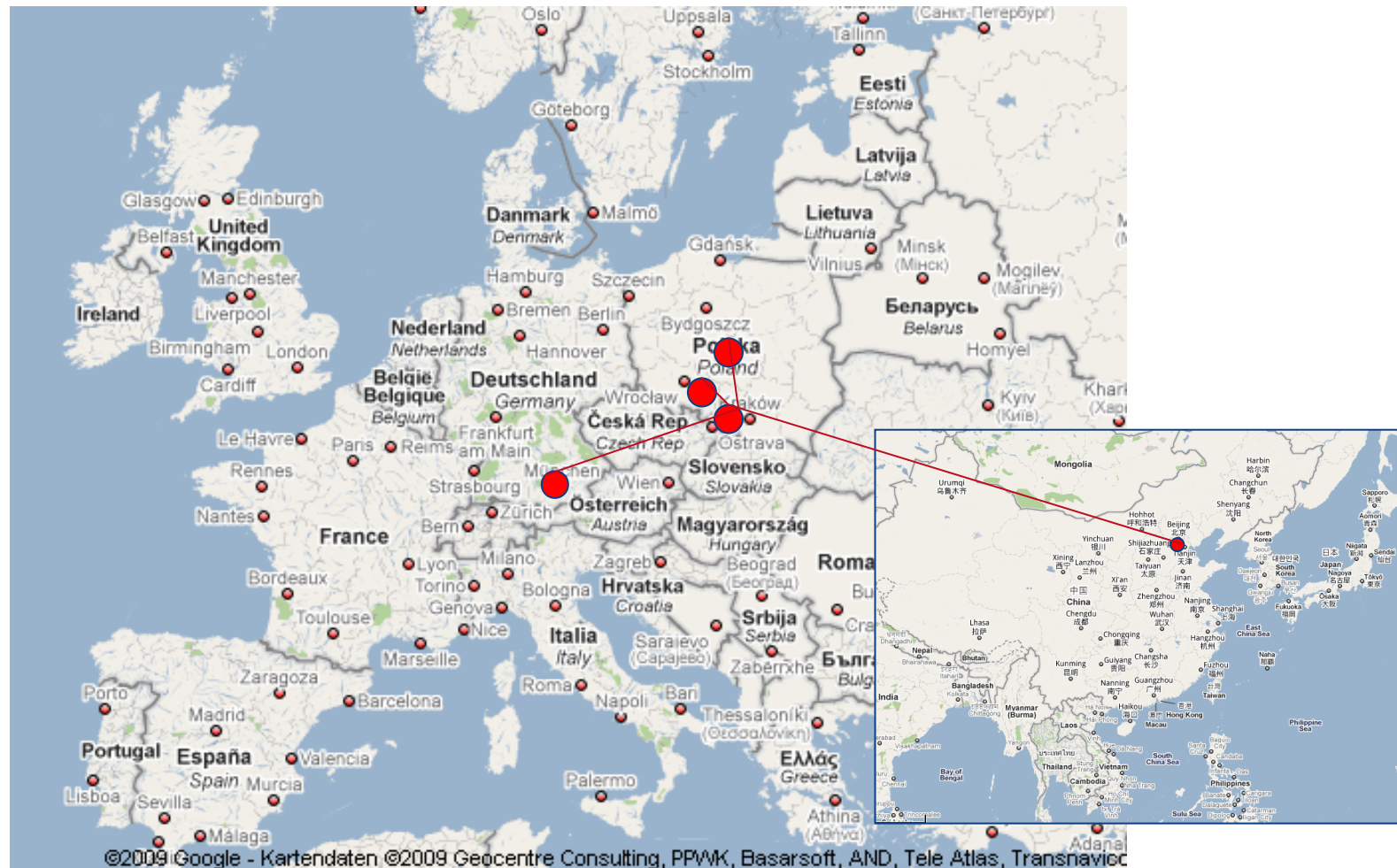


◆ Founded 1991

- **Dr. Wlodek Wrona**
 - ◆ Regional distribution, education
- **Dr. Wojtek Sakowski**
 - ◆ IP and design services
- **Privately held**
 - ◆ 90+ employees
 - ◆ 3 R&D facilities
 - ◆ 3 direct sales offices



THE EUROPEAN BASED GLOBAL COMPANY

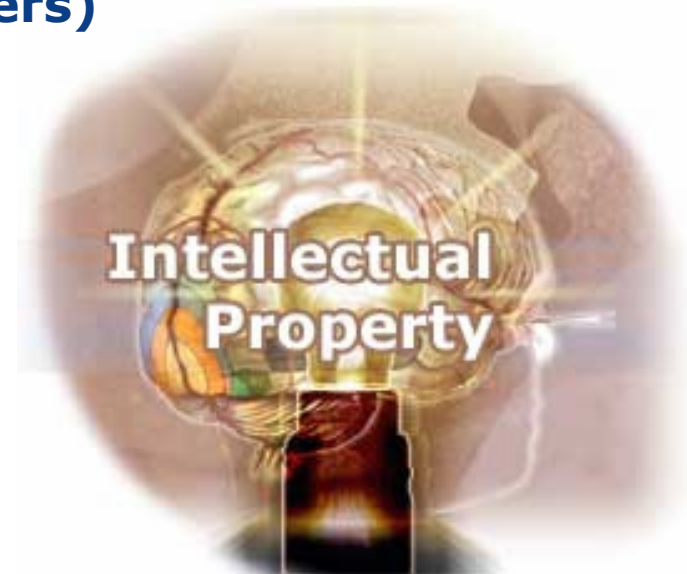


◆ IP division:

- 60+ engineers
- 600+ licenses (@ ~450 customers)
- 450+ silicon proven designs

◆ 3-pod business model

- IP cores „off the shelf“
- Design services
- Obsolete parts replacement



SOME OF EVATRONIX CUSTOMERS



◆ Processors

➤ 8 - 32 bit architectures

- ◆ x51 series, Z80, 6502
- ◆ 68k series
- ◆ 80186 series
- ◆ DSPs

◆ Interfaces

- ◆ USB 2.0, OTG & Hub
- ◆ USB 3.0
- ◆ Ethernet MACs



◆ Memory & Storage

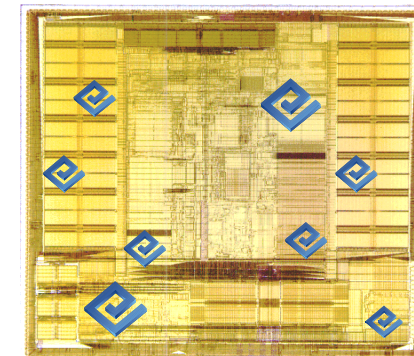
- ◆ NAND flash
- ◆ SDIO host
- ◆ ATA/IDE host

◆ Multimedia

- ◆ JPEG 2000 encoder
- ◆ Video Controllers (HD, TV)
- ◆ I2S, SPDIF

◆ Communication

- ◆ HDLC
- ◆ SDLC
- ◆ I2C, SPI

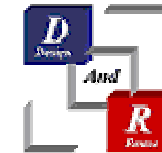


◆ Customer quote:

- **“.... and were highly impressed with the responsiveness, quality of work and professionalism of the company....”**
- *Director of Procurement, Fujitsu Telecommunications Europe Ltd*



◆ Partnerships & Associations



UNIVERSAL SERIAL BUS



◆ Research and Education

- **90% off list (netlist)**
- **Prototyping quantities**

◆ Commercial usage

- **80% off list (netlist)**
- **70% off list (source code)**
- **Prototyping quantities**



SUMMARY

- ◆ **Complete HW/SW solutions**
- ◆ **Experienced design and support team**
- ◆ **Silicon-proven IP**
- ◆ **Outstanding support**
- ◆ **Reliable business partner**
- ◆ **Strong links into the Research Community**
- ◆ **Very affordable CMP partner conditions**



Thank You

www.evatronix.com



evatronix
We turn engineering into art