

## STEERING COMMITTEE

B. Al-Hashimi  
K. Asada  
D. Bouldin  
B. Courtois (*chair*)  
M. Declercq  
D. Del Corso  
M. Glesner  
N. Guillemot  
A. Osseiran  
M. Rencz  
D. Donoval  
G. Gielen

UK  
Japan  
USA  
France  
Switzerland  
Italy  
Germany  
France  
Australia  
Hungary  
Slovakia  
Belgium



## SOCIAL EVENT

A dinner will be served in a charming hotel and gourmet restaurant castle named Le Château de la Commanderie, at only 10 minutes from Grenoble city center, on Thursday 10 May 2012.



During the social event, anybody who would like to say a few words (testimonials, suggestions, etc.) will have the possibility to do so. A few personalities have already expressed the wish to take advantage of this opportunity:

**François Anceau**, Founder CMP, UPMC/CNAM, France

**Dan J. Gale**, CTO, CMC, Canada

**Jean Michel Karam**, CEO, MEMSCAP, France

**Philippe Magarshack**, Technology R&D Group VP, STMicroelectronics, France

**Ricardo Reis**, UFRGS, Brazil

**Yuminosuke Yano**, Former President of Fujitsu Computer Technologies, Japan.

*A bottle of Cognac will award the funniest contribution.*

## PROGRAMME COMMITTEE

G. Jervan  
L. Torres  
L. Anghel  
Z-H. Wang  
M. Glesner  
N. Wehn  
W. Kuzmicz  
A. Bermak  
A. Rucinski  
D. Donoval  
E. Sicard  
D. Sciuto  
M. Rencz  
M. C. Johnson  
D. Bouldin  
K. Jeppson  
J. Nurmi  
C-L. Wey  
A. Ionescu  
G. Gielen  
R. Ernst  
Z. Navabi  
J. Nestor  
K. Masu  
A-K. Jones  
K. Cho

Estonia  
France  
France  
China  
Germany  
Germany  
Poland  
HK  
USA  
Slovakia  
France  
Italy  
Hungary  
USA  
USA  
Sweden  
Finland  
Taiwan  
Switzerland  
Belgium  
Germany  
Iran  
USA  
Japan  
USA  
Korea

## VENUE – LOCATION

The workshop is hosted at World Trade Center (WTC). WTC in the heart of Grenoble, located between train station and CMP.

“At the end of every street, a mountain”, said Stendhal. Located in the heart of the French Alps, Grenoble enjoys an exceptional geographical location and natural environment, making work and leisure easy to combine. The capital of the Alps benefits from an ideal geographic situation, at the same time close to the Lyon and Geneva airports.

CALL FOR PARTICIPATION

## 9th European Workshop on Microelectronics Education

Grenoble, France

May 9-11, 2012

Sponsored by:



**EWME 2012** is the ninth Workshop on microelectronics education to take place in Europe. The previous ones were held in Grenoble (1996), Noordwijkerhout (1998), Aix-en-Provence (2000), Vigo (2002), Lausanne (2004), Stockholm (2006), Budapest (2008), and Darmstadt (2010). **EWME** is held every second year in Europe, while **MSE** (Microelectronic Systems Education) is held every other year in the USA.

*Chair:* **Bernard Courtois**, CMP Grenoble, France

*Vice Chair:* **Sorin Cristoloveanu**, IMEP Grenoble, France

The Workshop will be the opportunity to celebrate the 30 years of CMP. Testimonials by world-renowned scientists, industry leaders and entrepreneurs will be offered.

[HTTP://CMP.IMAG.FR/CONFERENCES/EWME2012](http://CMP.IMAG.FR/CONFERENCES/EWME2012)

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## KEYNOTE

The Keynote will be delivered by **Andreas Wild**, Executive Director of ENIAC.

The ENIAC Joint Undertaking (JU) is a public-private partnership focusing on nanoelectronics that brings together Member/Associated States, the European Commission, and AENEAS (an association representing European R&D actors in this field).

## INVITED TALKS

"From MEMS at CMP to MEMSCAP" **Jean Michel Karam**, MEMSCAP, France

"Graphene as a new device material: A lesson for the future" **Young Kuk**, Seoul National Univ., Korea

"Deficiencies in Nanoscale CMOS device education" **Jerry Fossum**, Univ. of Florida, USA

"Cumulated experience from academia, industry and legal affairs" **Peter McLarty**, Texas Instruments, USA

"Match and mismatch between Industrial and academic perspectives on micro/nanoelectronics education and research" **Yoshio Nishi**, Stanford Univ., USA

"Fully-depleted SOI technology: a powerful architecture for the 20nm node and beyond" **Carlo Reita**, CEA-LETI, France,

"Which transistor path: FinFET, tri-gate, FDSOI, Ge/III-V, bulk CMOS...." **Thomas Skotnicki**, STMicroelectronics, France

"Teaching printed electronics" **Takao Someya**, Univ. of Tokyo, Japan

"Shaping the history of SPICE and the present of IC design and education" **Andrei Vladimirescu**, ISEP, France/UC Berkeley, USA,

"An undergraduate electronics curriculum for the 21st century" **Mark Law**, Univ. of Florida, USA

"Tomorrow's Engineer's Education – An attractive Europe Perspective? Europe's Excellence in Engineering Education, Research and Innovation" **Juergen Becker**, KIT, Karlsruhe, Germany

## PANEL

A panel of the Service Organizations will be discussing the following subject "At the time we approach the CMOS downsizing limits, is there still a need of us? What are the expectations of the Professors uncovered by us?"

*Moderator:* **Manfred Glesner**, TU Darmstadt, Germany

*Panelists:* **Ian McWalter**, CMC, Canada

**Wes Hansford**, MOSIS, USA

**Bernard Courtois**, CMP, France

**John McLean**, RAL, United Kingdom

**Kunihiro Asada**, VDEC, Japan

**Bin Wu**, ICC, China

**Tzi-Dar Chiueh**, CIC, Taiwan

**Kyoungrok Cho**, IDEC, Korea

## REGULAR ORAL SESSIONS

- CAD tools for education
- Cultivating non-technical skills in student projects
- Applications to support education
- E-learning
- Software tools to support education
- Industry and research Institutes relationships to education
- Education on testing and fault simulation
- Environments and methodologies for education
- Education on emerging technologies
- From the Lab to the room
- Education in emerging countries

## SPONSOR PRESENTATIONS

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