

GUIDE TO COMPLETE CMP ORDER FORM FOR ICs MANUFACTURING

This chapter explains how to complete items of “CMP ORDER FORM FOR ICs MANUFACTURING”. Variations for other order forms are explained at the end of this chapter. Check our web site “<http://cmp.imag.fr/>” when “check web in” is requested.

If you have the right software, you can complete directly the form in PDF format or in Word format. Otherwise, print the 2 pages and complete them with a typewriter.

Fax the form dated and signed before the transfer of your circuit. It contains technical data mandatory to check your circuit.

PAGE ONE

CMP RUN: name of the prototyping run (example S12C8_3). Check web in “PRODUCTS -> Run Schedule” for the list of names.

TOPCELL NAME: name of the top cell of your circuit. It must begin with a letter and is limited to 14 characters (letters and digits). This name is the name of your circuit. Avoid names with more than 14 characters.

PROCESS: technology of the circuit (example HCMOS9GP). Check web in “PRODUCTS -> Run Schedule” for the list of processes.

INSTITUTION/COMPANY: name of the institution or of the company that submits the circuit for fabrication. This name will appear in the list of circuits in the CMP’s annual report.

DELIVERY TO: Name of who should receive the circuit with his address (institution/company, road, building, town and country) and his phone number for a delivery of circuits by special mail. Delivery in a P.O. box is not allowed.

SPECIAL REQUEST ON DELIVERY: for special requests for delivery (special invoice for customs, ...).

INVOICE TO: Name and Email and address of who should receive the invoice after delivery of circuits. Invoicing address in the invoice will be the one of your purchase order.

P.O. number: Complete it and join your purchase order to this order form.

VAT Id. number: Mandatory for European countries, VAT identification number of the institution/company that is paying circuits.

PACKAGE: Reference of the chosen package or “naked dice”. Check web in “PRODUCTS -> Packaging Assembly” for the list of standard/big packages. For other packages, please contact CMP.

NUMBER: number of packaged circuits. Minimum number is 5.

BONDING DIAGRAM:

“free” CMP will make the bonding diagram and send it for agreement.

“imposed” The bonding diagram is imposed by your application. Join it to the CMP order form. You must draw every wire in cavity map. Check web in “DOWNLOAD -> Bonding diagrams” for cavity maps.

lids:

“removable” Lid is closed with a tape or points of glue and can be opened. Not possible for some packages like QFNs

“sealed” Lid is sealed. It can not be removed.

BULK MICROMACHINING: Must be selected if a post-process is requested for micromechanical system.

COLOR PLOTS: color plots must be requested before fabrication of the circuits. For delivery by mail, the plot is included in a rectangle of 110 cm x 75 cm.

NON-STANDARD THICKNESS: If you need circuits thinner than standard value (see page 3 of the CMP Order Form for a list), you can request it in this place. This service is charged for many technologies.

ADDITIONAL SERVICES: If you need a service that is not in this form, describe it here. CMP will quote you if a solution is found.

Name and signature of the responsible person: signature must be in the same page as the previous items. If page one is printed as 2 pages by your software, sign the page that contains information and complete and sign item “Name and signature of the responsible person” in the second page.

PAGE TWO

If you are using the html version of the document, please click on “NEXT PAGE” at the end of page one to get page two.

This page contains technical information about the circuit and is used to check your circuit and to replace abstracts of library cells by layouts. Missing data and errors can generate errors in the full layout version of your circuit. Please complete it carefully.

“**CMP RUN**”, “**TOPCELL NAME**” and “**PROCESS**” : Mandatory, must be the same as page one

TECHNICAL ENQUIRIES: MANDATORY. Name and coordinates of who can answer to questions about the circuit and can manage correction of errors. CMP will inform him of results of circuit checking and he will receive information about the run.

DESCRIPTION FORMAT: the main language is GDSII. The CIF language is allowed if and only if your CAD tool can not generate GDSII. First contact CMP if you need to use DEF format or CADENCE data base.

CAD TOOL: name and version of the CAD software used to generate layout. If you are using CADENCE, type “icfb -W” to get the version number.

DESIGN KIT USED: name and version of the design kit used to generate layout. Mandatory if the design kit is provided by CMP or by the foundry. For home made design kits, write “home made” after the name.

DRC VIOLATIONS: Circuits must be free of DRC errors for fabrication. Run the “signoff” DRC of the foundry on the circuit before submission. If you need DRC violation for special device, make a case 1 week before the deadline of the run to request it. CMP will forward your request to the foundry for an agreement. A case must describe the special device and why DRC violation is requested to make it. Note that the foundry can reject your request.

LIBRARY CELL REPLACEMENT REQUIRED: In many design kits provided free of charge by CMP, library cells contain only a reduced layout without transistors. As a result, these library cells must be replaced by the full layout version. In this case select “yes” and complete the list of used libraries. Note that DRC do not detect reduced layouts.

LIBRARY NAME: Must be completed when library cell replacement is required. Write the list of used libraries with versions. Only cells of the listed libraries will be replaced by full layouts:

- If you have a full custom cell with the same name as a cell of a requested library, the full custom cell will be overwritten by the library cell.
- If you have forgotten to request replacement of cells of a library, you are responsible of empty cells contained in the circuit.

CIRCUIT'S FUNCTION: Function of the circuit in a few lines. Mandatory for non E. U. countries, the function is necessary to request an export license. Function is mandatory for technologies of STMicroelectronics.